

<b>Form PTO-1449 Modified</b>  List of Patent and Publications Cited by Applicant (Use several sheets if necessary)  U.S. Department of Commerce Patent and Trademark Office				Docket No. <b>MSFT-2865/304048.02</b>		Application No. <b>10/777,797</b>	
				Applicant <b>Anuj B. Gosalia, et al.</b>			
				Filing Date <b>February 12, 2004</b>		Group <b>2182 2195</b>	
				Confirmation No. <b>5844</b>			
<b>U. S. PATENT DOCUMENTS</b>							
<b>Examiner Initial</b>		<b>Document No.</b>	<b>Date</b>	<b>Name</b>	<b>Class</b>	<b>Subclass</b>	
/A.K./	5	6,081,854	06/27/00	Priem, C. et al.	710	37	
/A.K./	6	2002/0118202 A1	08/29/02	Baldwin, D.R.	345	530	
<b>FOREIGN PATENT DOCUMENTS</b>							
<b>Examiner Initial</b>		<b>Document No.</b>	<b>Date</b>	<b>Country</b>	<b>Translation</b>		
					<b>YES</b>	<b>NO</b>	
<b>EXAMINER</b>				<b>DATE CONSIDERED</b>			
/Abdullah Al Kawsar/				01/15/2008			

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<b>OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)</b>			
	<b>4</b>	Torvalds, L. et al., "Excerpt from the Linux Kernel", version 2.4, 2002, <u><a href="http://www.kernel.org/pub/linux/kernel/v2.4/linux-2.4.20.tar.bz2">http://www.kernel.org/pub/linux/kernel/v2.4/linux-2.4.20.tar.bz2</a></u> , 1-87, Internet site was unavailable.	
<b>EXAMINER</b> /Abdullah Al Kawsar/		<b>DATE CONSIDERED</b> 01/15/2008	



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	Applicant <b>Steve Pronovost, Anuj B. Gosalia, Bryan L. Langley and Hideyuki Nagase</b>	
	Filing Date <b>February 12, 2004</b>	Group <b><del>Not Yet Assigned</del> 2195</b>
<b>OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)</b>		
/A.K./	1	Burgess, P. et al., "BED: A Multithread Kernel for Embedded Systems", <i>Real Time Programming, IFAC Workshop, 1994</i> , 133-138
/A.K./	2	Macedonia, M., "The GPU Enters Computing's Mainstream", <i>Computer, October 2003</i> , 36(10), 106-108
/A.K./	3	Petrot, F. et al., "Lightweight Implementation of the POSIX Threads API for an On-Chip MIPS Multiprocessor with VCI Interconnect", <i>Proceedings Design, Automation and Test in Europe Conference and Exhibition, March 3-7, 2003, Munich, Germany</i> , 51-56
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	Filing Date February 12, 2004	Group <del>2182</del> 2195.
	Confirmation No. 5844	

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Examiner Initial		Document No.	Date	Name	Class	Subclass
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A. K.	2	5,896,141	04/20/99	Blaho et al.	345	541
A. K.	3	5,918,050	06/29/99	Rosenthal et al.	718	108
A. K.	4	6,023,738	02/08/00	Priem et al.	710	23
A. K.	5	6,049,857	04/11/00	Watkins	711	207
A. K.	6	6,065,071	05/16/00	Priem et al.	710	22
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A. K.	9	6,446,186	09/03/02	Priem et al.	711	206
A. K.	10	6,518,973	02/11/03	Blythe	345	564
A. K.	11	6691180	02/10/04	Priem et al.	710	23

### FOREIGN PATENT DOCUMENTS

Examiner Initial		Document No.	Date	Country	Translation	
					YES	NO
	12	WO 02/09083	01/31/02	PCT		

EXAMINER <i>Abdullah Al Karim.</i>	DATE CONSIDERED <i>01/17/08</i>
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<b>NON-PATENT DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)</b>			
<b>A.K.</b>	<b>13</b>	Intel Corp, "Accelerated Graphics Port Interface Specification, Revision 1.0," July 1996, 1-161	
<b>A.K.</b>	<b>14</b>	Martin, K.E. et al., "Direct Rendering Infrastructure, Low-Level Design Document," <a href="http://dri.sourceforge.net/doc/design_low_level.html">http://dri.sourceforge.net/doc/design_low_level.html</a> , May 1999, 1-18	
<b>A.K.</b>	<b>15</b>	Owen, J. et al., "A Multiple Direct Rendering Architecture for 3D," <a href="http://dri.sourceforge.net/doc/design_high_level.html">http://dri.sourceforge.net/doc/design_high_level.html</a> , September 1998, 1-10	

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